Design Services Partner Profile

AMD
XILINX

Orthogone Technologies Inc Company Overview

Date : 03/23/2021

www.orthogone.com

Partner Details		
Partner Tier:	Premier	
Headquarters Location:	2250 Alfred-Nobel Boulevard, Suite 500,Saint-Laurent,QC,Canada	
Xilinx Alliance Partner Since:	10/15/2012	

Key Contact Info

Sylvie Briere
sbriere@orthogne.com

Regions Served

Supported Product Types				
Yes	Design Services	No	Embedded Products
Yes	IP	No	EDA Tools
No	Boards		

Engineering Service Types			
Yes	General Purpose Services	Preferred
Yes	Services in Support of Products Sold	
Yes	Services Leading to Contract Manufacturing	
Maximum Service Scope:	Turnkey / full product design	

Engagement Models Supported			
Yes	SOW-Based Fixed Bid Projects	Preferred
Yes	Time and Materials	
Both	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience				
Yes	Vivado	Yes	SDSoC
No	HLx	No	SDAccel

Industry Certifications Achieved

Industry Certification Services Offered   CE, FCC, Other / Contact Partner, UL

Government Clearances   Controlled Goods Program (Canada)

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Orthogone Technologies Inc Segment and Application Focus

Secondary Focus	**Defense/Aerospace**	No Focus	Homeland Security
		No Focus	Electronic Warfare
		No Focus	ISR
		Secondary Focus	Space
		No Focus	Missiles & Munitions
		No Focus	Military Avionics
		Secondary Focus	Commercial Avionics
		Secondary Focus	SatComm
		Primary Focus	Wireless Milcom
		No Focus	Other A&D

Primary Focus	**Audio, Video, Broadcast**	Secondary Focus	Encoders & Decoders
		Primary Focus	Monitors & Projection
		Primary Focus	Networking & Converters
		Primary Focus	Audio Systems
		Primary Focus	Video Processing Cards
		No Focus	Video Conferencing
		Secondary Focus	Routers and Switches
		No Focus	Pro Cameras & Camcorders
		No Focus	Other Broadcast

Secondary Focus	**Automotive & Transport**	Primary Focus	Driver Assist - Camera
		Primary Focus	Driver Assist Radar/LIDAR
		Primary Focus	Driver Info/Infotainment
		Primary Focus	Automated Driving
		Primary Focus	Other Automotive

Secondary Focus	**Consumer**	Primary Focus	Digital Display
		No Focus	Drones
		No Focus	Multifunction Printers
		No Focus	Other Consumer

Primary Focus	**Datacenter**	No Focus	Storage
		Primary Focus	Compute
		Primary Focus	Networking

Secondary Focus	**Industrial, Medical**	No Focus	Motor Control
		Secondary Focus	Industrial Networking
		Primary Focus	Surveillance
		Primary Focus	Machine Vision
		Primary Focus	Medical Imaging
		No Focus	Medical / Clinical
		No Focus	Other ISM

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Orthogone Technologies Inc Segment and Application Focus (continued)

Secondary Focus	**Test & Measurement**		
		No Focus	Simulation / Emulation
		Secondary Focus	T & M Instrumentation
		No Focus	Semiconductor Test
		Secondary Focus	Wired Communication Test
		Secondary Focus	Wireless Communications Test
		Secondary Focus	Other Test & Measurement

Primary Focus	**Wired Comms & Networks**		
		Primary Focus	Access Equipment
		Secondary Focus	Long Haul/Transport
		Primary Focus	Wired Mobile Backhaul
		No Focus	OTN
		Primary Focus	Enterprise Equipment
		No Focus	Ethernet Interlaken Connectivity
		Primary Focus	Packet Processing
		No Focus	Other Wired Communications

Primary Focus	**Wireless Comms & Networks**		
		Primary Focus	Baseband
		Primary Focus	Wireless Backhaul
		Primary Focus	Connectivity & Switching
		Primary Focus	Radio
		No Focus	Other Wireless

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Design Competencies

System Architecture	Primary Focus	Expertise in wireless/wired infrastructure, LiDAR sub-system, embedded vision, display interfaces, IoT
RF and Analog Design	Primary Focus	RF: Microwave backhaul & small cells design (baseband to antenna). WiFI/BT Modules, 4G Modules, GPS/GNSS chip/module. Analog: Power Supply design, clock gen. and distribution (DAC/ADC), laser driver, photodiode array, etc.
FPGA Design and Integration	Primary Focus	FPGA IP Cores FPGA Architecture, Design, Verification (UVM-Based), Test & Integration
FPGA Timing Closure	Primary Focus	Timing closure
FPGA Design Optimization	Primary Focus	Area reduction: e.g. Time-slice techniques. Low-power consumption (e.g. SFP+ module, low-power memory controller, etc.)
Signal Integrity Design and Analysis	Primary Focus	SerDes Links (jitter link budget, IBIS-AMI, crosstalk, etc.) DDR-3/4 Memory simulations (timing closure, signal integrity)
Inhouse or Subcontracted:	Inhouse	
Typical Serial Speed:	1025	
PCB Design & Layout	Primary Focus	High-speed layout. High-Density Interconnect (ball pitch down to 0.4mm). RF, Analog, high-speed digital, etc.
Inhouse or Subcontracted:	Both	
Industrial and Mechanical Design	Secondary Focus	Design partner (industrial design and mechanical engineering)
Inhouse or Subcontracted:	Subcontracted	
Industry Certification and Compliance Testing	Secondary Focus	Generaly provide pre-certification design services (e.g. EMI/EMC, UL, CE, FCC, HDMI, etc.)
Inhouse or Subcontracted:	Both	
Quality and Reliability Testing	Secondary Focus	MTTF, MTBF Analysis, HALT Testing (partners)
Inhouse or Subcontracted:	Subcontracted	
Contract Manufacturing	Secondary Focus	NPI (CM partner)
Inhouse or Subcontracted:	Subcontracted	
Max Quantities Supported:	None	
Digital Signal Processing	Primary Focus	Audio DSP, Radar applications
Experience:	TI OMAP, Myriad (Intel), CSR (QualComm)	
Embedded Processors	Primary Focus	Micro-controller, Micro-processor (processing, multi-media), FPGA embedded, General Purpose Processor
Experience:	TI, QualComm 8xx, NXP i.MX-	

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	6/7/8, Renesas R-CAR	
Embedded Porting / BSP Extension	Primary Focus	BSP, Embedded OS/RTOS development and porting.
OS and RTOS Design Experience:	Android, FreeRTOS, Linux, QNX, ThreadX, Windows Embedded	
Embedded Application Development	Primary Focus	API and Device drivers, communication stacks (Ethernet, TCP/IP, PCIe, USB, etc.), security frameworks, etc.
Image Sensor Fusion	No Focus	
Homo or Heterogeneous:		
Sensor Types Design Experience:		
Computer Vision	Primary Focus	Automotive LiDAR, frame grabbers, image sensors and cameras, medical imaging application, (ultra high resolution sensor)
Machine Learning	Secondary Focus	Image classification
Framework Experience:		
Network Model Experience:		