DornerWorks Unveils Virtualized Embedded Encryption Demo at Xilinx Developer Forum

The Alliance Program Premier Partner has been developing secure solutions with Xilinx technology for over a decade

SAN JOSE, Calif., Oct. 2, 2018 (Xilinx Developer Forum) - DornerWorks, a technology engineering firm headquartered in Grand Rapids, Mich., has pushed the possibilities of virtualized embedded systems further than ever before with a new AES Encryption demo at the Xilinx Developer Forum (XDF) 2018. XDF connects software developers and system designers to the deep expertise of Xilinx engineers, partners, and industry leaders.

The demonstration will show the differences in speeds between software and hardware based **AES encryption and decryption** processes on a Xilinx Zynq UltraScale+ MPSoC.

Xilinx has been fostering key partnerships with companies like Amazon Web Services (AWS) to put FPGA programmability by way of C, C++ and OpenCL into the hands of more and more developers, many who will be visiting San Jose this October for the U.S. leg of the XDF circuit.

Where AWS is helping connect Xilinx developers through the cloud, DornerWorks is providing access to exclusive technical support and product design services. DornerWorks has been working with Xilinx technology for years, and as an **Alliance Program Premier Partner**, is positioned to help developers make the most of their FPGA, MPSoC and RFSoC projects with collaboration and expert guidance.

DornerWorks is also proud to be listed as an exhibitor at XDF this fall, where DornerWorks engineers will be showing off the latest in embedded virtualized hardware systems. Built for ARM and x86 targets, DornerWorks cost-effective, platform-enabling Virtuosity OA software was rigorously tested on the Xilinx Zynq UltraScale+ MPSoC. The combination of Virtuosity and a strong processor make for a revolutionary new way to run disparate software architectures concurrently, and safely, with robust isolation and partitioning.

"Embedded systems enhanced by AES encryption can provide tremendous value to companies looking to implement security without sacrificing system performance in their product development," says DornerWorks President and Founder David Dorner. "DornerWorks has built a number of custom IP cores that can be easily integrated into existing frameworks with FPGA technology and Xilinx silicon, and this demonstration will show just how effective those cores can be."

"DornerWorks, a Xilinx Premier partner, has been supporting customers using Xilinx devices and development tools for years," says Simon George, director of system software and SoC solution marketing, Xilinx. "DornerWorks' strong hardware, software and systems engineering teams have accelerated design projects, optimized system performance, and enhanced security for customers who choose Xilinx technology. We're looking forward to seeing them push innovation even further at XDF and beyond."

More information about XDF is available at: https://www.xilinx.com/products/design-tools/developer-forum/silicon-valley.html. For additional updates from XDF, follow Xilinx on Twitter at @XilinxInc or via the hashtag #XDF2018.

About DornerWorks:

DornerWorks provides technology engineering so you can focus on your customers. With embedded electronics, FPGA, and software engineering expertise, we accelerate your product development and lower risk for adopting advanced technologies. One of our specialties is embedded virtualization technology, including Xen Project hypervisor and seL4 secure microkernel-based solutions. For more information, visit www.DornerWorks.com.

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