

Design Services Partner Profile



Missing Link Electronics, Inc. Company Overview

Date : 11/28/2023

<http://www.missinglinkelectronics.com>

Partner Details

Partner Tier: Premier
Headquarters Location: 2880 Zanker Road, Suite 203, San Jose, CA, USA
Xilinx Alliance Partner Since: 2/14/2011

Key Contact Info

Andreas Schuler
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Regions Served

Supported Product Types

Yes	Design Services	Yes	Embedded Products
Yes	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services	Preferred
Yes	Services in Support of Products Sold	
Yes	Services Leading to Contract Manufacturing	
	Maximum Service Scope: Turnkey / full product design	

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
Yes	Time and Materials	
Both	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	Yes	SDSoC
Yes	HLx	Yes	SDAccel

Industry Certifications Achieved

Industry Certification Services Offered

Government Clearances

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Missing Link Electronics, Inc. Segment and Application Focus

Secondary Focus	Defense/Aerospace	<ul style="list-style-type: none"> Secondary Focus No Focus No Focus No Focus No Focus Secondary Focus No Focus No Focus Secondary Focus Secondary Focus 	<ul style="list-style-type: none"> Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
Secondary Focus	Audio, Video, Broadcast	<ul style="list-style-type: none"> No Focus No Focus Secondary Focus No Focus No Focus No Focus No Focus No Focus Secondary Focus 	<ul style="list-style-type: none"> Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
Primary Focus	Automotive & Transport	<ul style="list-style-type: none"> Secondary Focus Secondary Focus Secondary Focus Primary Focus Primary Focus 	<ul style="list-style-type: none"> Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
No Focus	Consumer	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Digital Display Drones Multifunction Printers Other Consumer
Primary Focus	Datacenter	<ul style="list-style-type: none"> Primary Focus Primary Focus Primary Focus 	<ul style="list-style-type: none"> Storage Compute Networking
Secondary Focus	Industrial, Medical	<ul style="list-style-type: none"> Primary Focus Primary Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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Missing Link Electronics, Inc. Segment and Application Focus (continued)

Secondary Focus	Test & Measurement	<ul style="list-style-type: none"> No Focus No Focus Secondary Focus Secondary Focus Secondary Focus Secondary Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
Secondary Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus Secondary Focus Secondary Focus No Focus No Focus No Focus Primary Focus No Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
Secondary Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus Secondary Focus Secondary Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	Primary Focus	Very high; HW SW co-design, Performance modeling, system-level accel, HLS, IEEE 802.1, IEEE 802.3, IEEE 1588
RF and Analog Design	Secondary Focus	High; RFSoc, Soft Analog IP Cores, JESD204B, DSP
FPGA Design and Integration	Primary Focus	Very High; VHDL, Verilog implementation, full design flow with vendor provided and 3th party tools
FPGA Timing Closure	Secondary Focus	Very high, including high speed serdes, iMux, NOC
FPGA Design Optimization	Primary Focus	Very high, resource optimization, retargeting from Virtex to Ultrascale to Ultrascale+, Versal
Signal Integrity Design and Analysis	Secondary Focus	High, SATA, SAS, UFS, PCIe Gen1 - Gen5, JESD204B, Aurora, 58G PAM 4
	Inhouse or Subcontracted: Both Typical Serial Speed: 12	
PCB Design & Layout	Primary Focus	High, including specifics for FPGA
	Inhouse or Subcontracted: Both	
Industrial and Mechanical Design	Secondary Focus	Inhouse and with certified partners
	Inhouse or Subcontracted: Both	
Industry Certification and Compliance Testing	Secondary Focus	Medium, SIL, EN 61508, ISO26262, IEC 60601
	Inhouse or Subcontracted: Both	
Quality and Reliability Testing	No Focus	
	Inhouse or Subcontracted:	
Contract Manufacturing	Secondary Focus	via certified partners
	Inhouse or Subcontracted: Both	
	Max Quantities Supported: 10,000+	
Digital Signal Processing	Secondary Focus	High, FPGA conversion of DSP, Computer Vision, beam forming
	Experience: TMS320C6678	
Embedded Processors	Primary Focus	Very high, including HW/SW interfaces, Linux, Kernel, FSBL, Uboot, DTS, BSP, XPPU, XPMU, OP-TEE
	Experience: Zynq MicroBlaze, MSP430, PowerPC, x86	
Embedded Porting /	Primary Focus	Very high, including SMP/AMP, IPI, AutoSar, Baremetal,

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BSP Extension		BroadR-Reach
OS and RTOS Design Experience:	FreeRTOS, Linux, Ubuntu Linux	
Embedded Application	Secondary Focus	High, Linux applications, Computer Vision, machine learning
Development		
Image Sensor Fusion	Primary Focus	Active Pedestrian Detection, Automated Car Testing Ground, camera-monitor systems (ISO 16505), ToF distance measuring under SIL
Homo or Heterogeneous:	Both	
Sensor Types Design Experience:	GPS / Position, Gyro, LIDAR, Other / Contact Partner, Radar, Thermal, Video (4K, 8K), Video (SD, HD)	
Computer Vision		
Machine Learning	Primary Focus	Industrial vision PoC on Virtex-7 690; Industrial robot demonstrator, AI Edge inference, Acceleration and offloading
Framework Experience:		
Network Model Experience:		
