

## **Turbo-Charge Your Insight**

Presented By



Tanya Roosta, PhD Lead Al Researcher October 2<sup>nd</sup> 2018 Jim Wu Lead Acceleration Engineer



## TURBO CHARGE YOUR INSIGHT

# SUMUP ANALYTICS

#### THE PAIN POINT

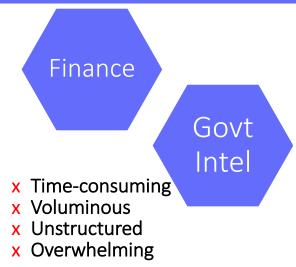
Too Much To Read

Time Is Scarce

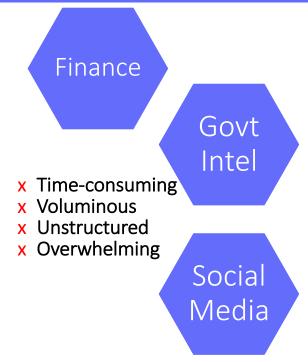
- x Time-consuming
- x Voluminous
- x Unstructured
- x Overwhelming



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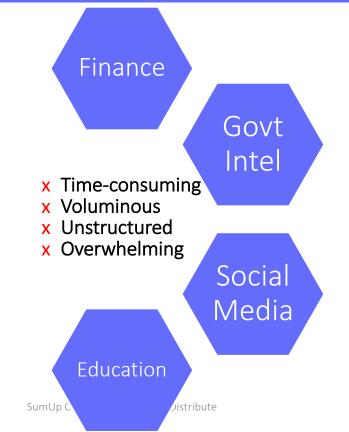


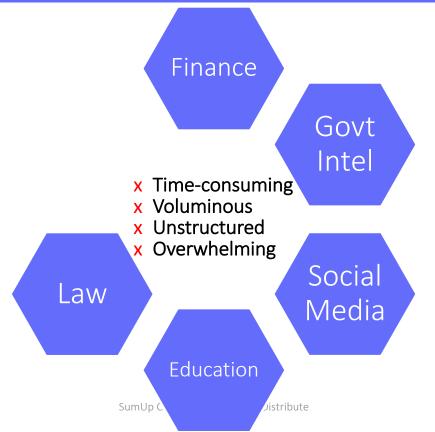
### High-granularity real-time analysis on Unstructured data

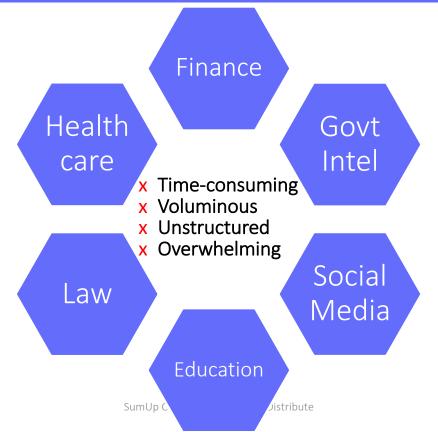


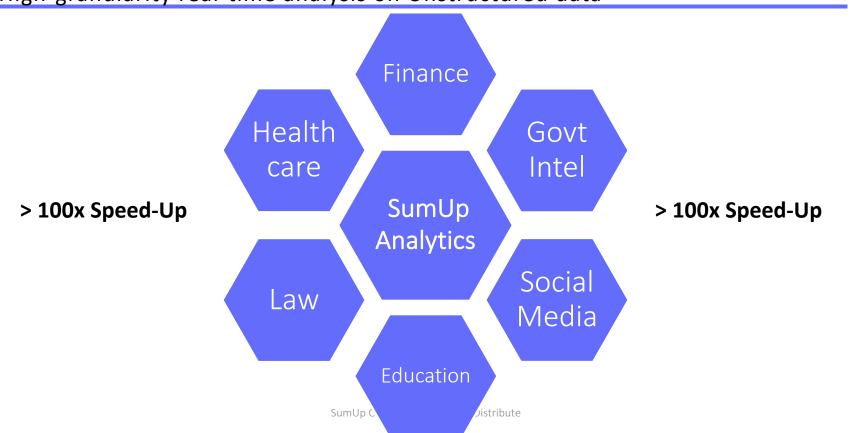
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#### **OUR SOLUTIONS**

**Nucleus SaaS** 

**Streaming Analytics APIs** 

**On-Demand Analytics APIs** 

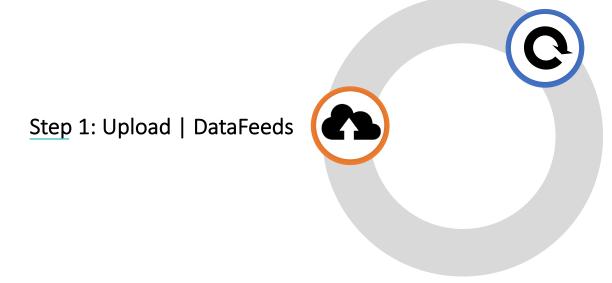


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**Streaming Analytics APIs** 

**On-Demand Analytics APIs** 



#### Step 2: Iterative Analysis

- Identify Topics & Summarize
- Analyze Sentiment & Consensus
- Get document recommendations
- Deep-dive on-demand

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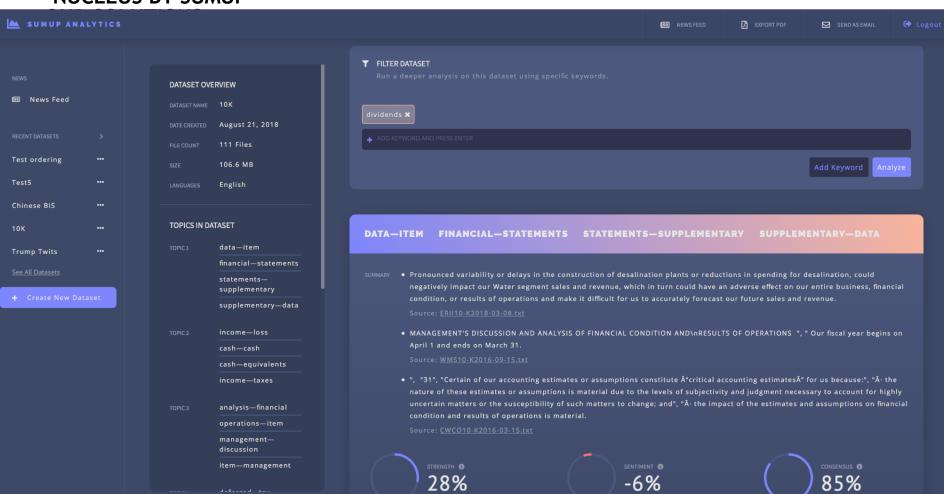
Step 1: Upload | DataFeeds

#### Step 2: Iterative Analysis

- Identify Topics & Summarize
- Analyze Sentiment & Consensus
- Get document recommendations
- Deep-dive on-demand

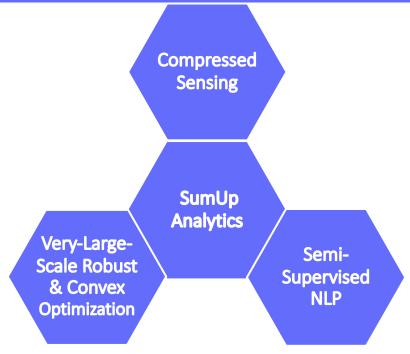
Step 3: Export & Share

#### **NUCLEUS BY SUMUP**



#### DIFFERENTIATED INTELLECTUAL PROPERTIES

**R&D** in Analytics & Computational Systems

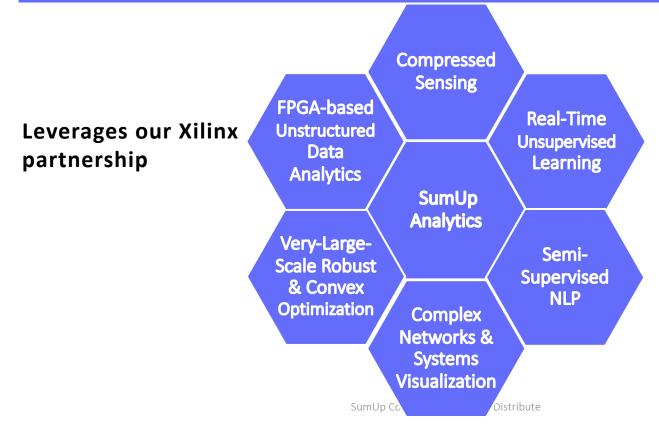


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Distribute

#### DIFFERENTIATED INTELLECTUAL PROPERTIES

R&D in Analytics & Computational Systems



#### FPGA: NATURAL FIT FOR UNSTRUCTURED DATA



FPGA very appealing to implement AI algos for Unstructured Data

#### FPGA: POWERS NUCLEUS END TO END

Workflow

Raw Text Upload Raw Text Preprocessing Core AI-based Analytics

Peripheral Albased
Analytics

FPGA Solution

FPGA+PCle FPGA+SSD boards Vectorization, Tokenization, Regexp IPs

Text AI algos on FPGA

Text AI algos on FPGA

#### FPGA: POWERS NUCLEUS END TO END

Peripheral AI-Core Al-based Raw Text Raw Text based Workflow **Upload Preprocessing Analytics Analytics** Vectorization, FPGA+PCle Text AI algos **FPGA** Text AI algos FPGA+SSD Tokenization, on FPGA on FPGA Solution boards Regexp IPs Fits in single FPGA chip, min. data transfer

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Peripheral AI-Core Al-based Raw Text Raw Text based Workflow Upload **Preprocessing Analytics Analytics** Vectorization, FPGA+PCle Text AI algos **FPGA** Text AI algos FPGA+SSD Tokenization, on FPGA on FPGA Solution boards Regexp IPs Fits in single FPGA chip, min. data transfer Takes out CPU, max I/O

#### ACCELERATED DEV & DEPLOY W/ XILINX

Xilinx Public IPs

SumUp Python/SDAccel Wrapper

AWS F1

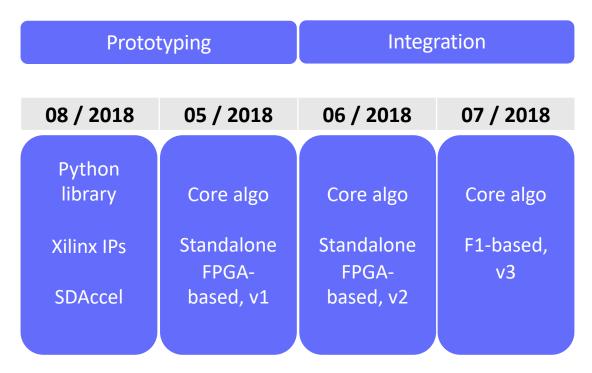
## Prototyping 08 / 2018 05 / 2018 Python library Core algo Standalone Xilinx IPs FPGA-**SDAccel** based, v1

#### ACCELERATED DEV & DEPLOY W/ XILINX

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AWS F1



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#### **ACCELERATED DEV & DEPLOY W/ XILINX**

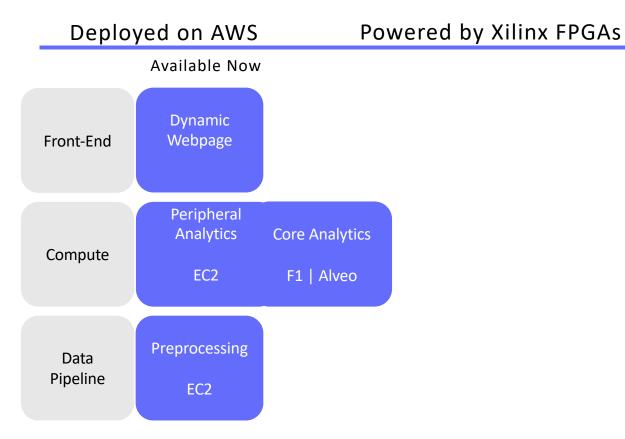
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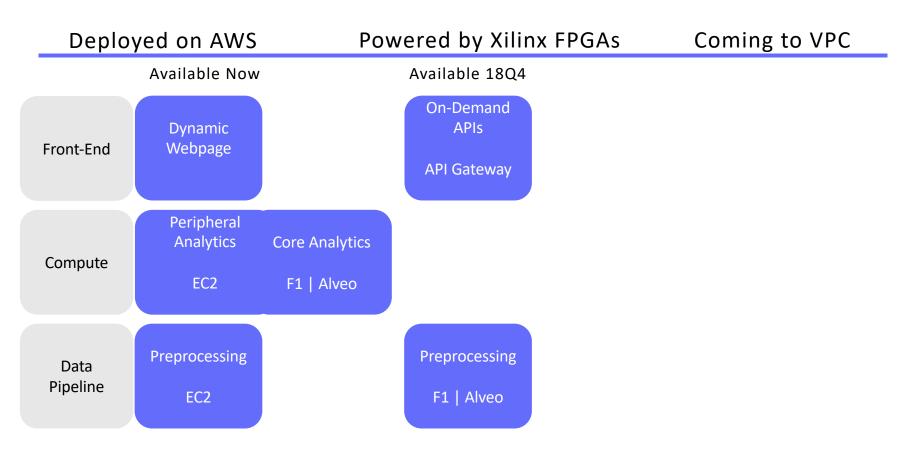
Prototyping		Integ	ration	Deployment		
08 / 2018	05 / 2018	06 / 2018	07 / 2018	08 / 2018	10 / 2018	
Python library Xilinx IPs	Core algo Standalone	Core algo Standalone	Core algo F1-based,	Commercial library	Commercial library	
SDAccel	FPGA- based, v1	FPGA- based, v2	v3	F1-deployed	Alveo-ready	

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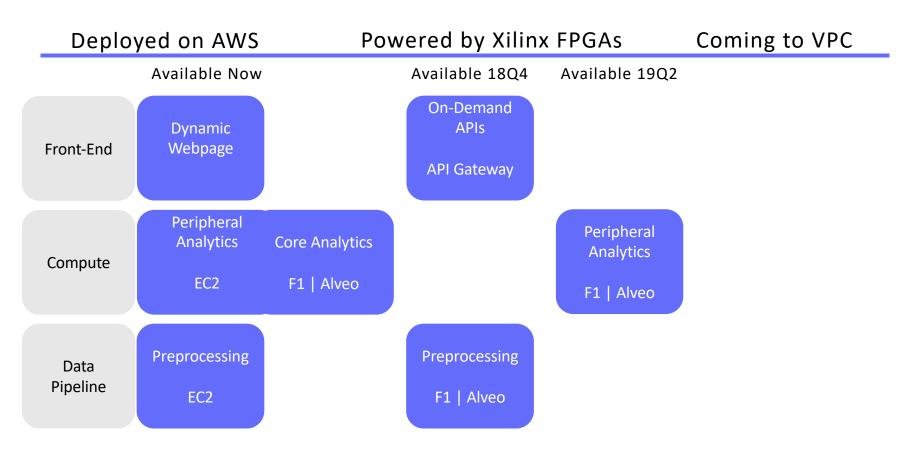


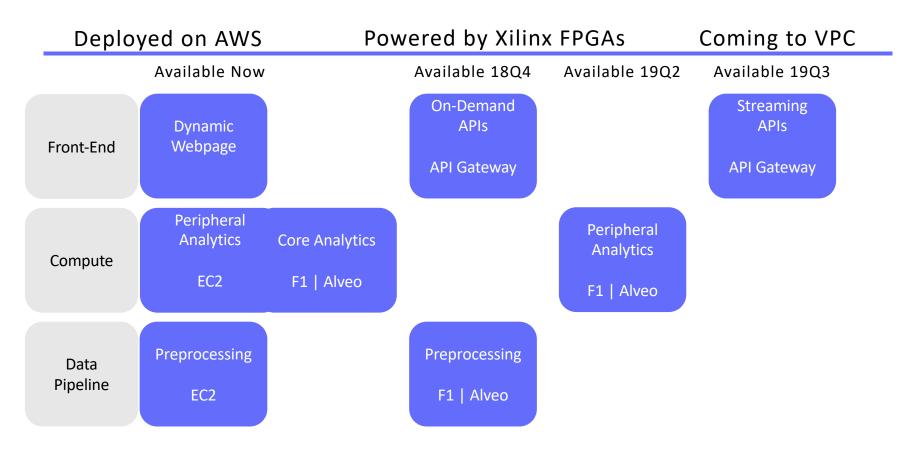
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Coming to VPC



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#### **BENCHMARKS**

#### > 100x Faster

Nhadaa	Dictionary length	Nbr topics	Runtime in seconds			U_Mass Coherence		
Nbr docs			Nucleus	Scikit	Gensim	Nucleus	Scikit	Gensim
31,496	81,801	1	0.1	230	10	-9.7	-19.9	-19.9
		4	0.3	648	32	-9.4	-14.7	-15.5
		8	0.6	1157	51	-11.9	-11.8	-16.0
29,287	78,063	1	0.1	220	10	-4.9	-5.8	-18.2
		4	0.2	588	36	-9.8	-14.9	-16.9
		8	0.7	1023	57	-8.3	-15.8	-16.3
158,348	336,532	1	0.4	2817	98	-4.8	-14.0	-18.0
		4	1.0	11282	371	-6.8	-12.7	-17.6
		8	1.7	N/A	685	-6.9	N/A	-16.0
2,620,008	3,253,518	1	9	39600	8135	-10.0	-15.5	-15.4
		4	21	N/A	34850	-9.0	N/A	-16.8
		8	34		N/A	-6.3		N/A
7,539,661	7,817,625	1	20	N/A		-2.5		
		4	54		N/A	-3.5	N/A	N/A
		8	113			-5.8		

#### Much higher topic quality

U\_Mass: less negative is better



AWS c4.8xlarge, f1.2xlarge N/A indicates run not completed within 36 hours

#### **DEMO**

## Alpha Generation in Equities

1day 14h -> 3min 40s

 Portfolio managers, financial analysts, quants See for yourself

Social Media Monitoring

6days -> 55min

 Intelligence analysts, compliance officers, data scientists

#### IN SUMMARY

- Nucleus by SumUp offers the fast, flexible and transparent text analytics solution needed by professionals
- Nucleus typically is > 100x faster than alternative solutions
- Further acceleration is achievable through complete integration of data pipeline and algos on FPGA board

