



Vitis

A Unified Software Platform to Enable System Design for Hardware and Software Developers

# BOOST PRODUCTIVITY. ACCELERATE PERFORMANCE.

Design complexity demands are growing across markets. To meet these demands, system design tools need to empower hardware and software developers to work together.

The AMD Vitis™ unified software platform enables developers to build advanced, performance-optimized designs with AMD adaptive SoCs and FPGAs.

## STREAMLINE DEVELOPMENT



### DESIGN FAST

Tap into a comprehensive software and hardware design environment made for AMD adaptive SoCs and FPGAs with tools to design, simulate, and implement complex systems.

## ACCOMPLISH MORE



### Speed up design cycles

Quickly optimize and compile complex designs that use programmable logic and AI Engines.



### Jump-start designs

Access domain-specific and commonly used libraries for programmable logic and AI Engine-based designs.



### Simplify complexity

More easily simulate, implement, and debug mixed-domain designs.

## WORK SMARTER, ENHANCE DESIGNS

### FLEXIBILITY

Design, debug, and compile your software code with **Vitis™ Embedded**.

### PERFORMANCE

Achieve exceptional performance through **embedded or data center acceleration**.

### EFFICIENCY

Explore, simulate, and verify designs with **Vitis™ Model Composer** for the MATLAB® / Simulink® environment.

### PRODUCTIVITY

Get fast design implementation and simulation using **Vitis™ high-level synthesis**.

## BUILT FOR HIGH-PERFORMANCE AMD ADAPTIVE DEVICES

Adaptive SoCs



**CAPTURE THE AI ENGINE ADVANTAGE**

- Improve digital signal processing performance per watt
- Accelerate compute capability
- Improve signal processing throughput

FPGAs



## VITIS™ UNIFIED SOFTWARE PLATFORM

Develop adaptive system designs fast. Deliver on performance and power.

[Learn More](#)

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\*Some Versal™ Premium devices offer AI Engines. Refer to the data sheet for the latest list of devices.

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