Versal[™] Prime Series VMK180 Evaluation Kit

OVERVIEW

The Versal[™] Prime series VMK180 evaluation kit, equipped with the VM1802 ACAP, enables the fastest path to application design using the Versal architecture.

The VMK180 allows designers to jump-start their hardware/software differentiation with pre-built system infrastructure, enabled by a programmable network on chip (NoC) and integrated shell of optimized IP.

With high-performance compute engines, next-generation I/O, and standardized development flows, the VMK180 is an ideal evaluation kit for designing diverse workloads in a wide range of markets.

HIGHLIGHTS

Evaluate Versal Prime Series Capabilities

- > Equipped with Versal ACAP VM1802 production silicon
- > Accelerate hardware/software differentiation with pre-built system infrastructure including integrated shell and programmable NoC
- > Pre-optimized reference designs for rapid prototyping

Breadth of Connectivity Options for Rapid Development

- > PCle® Gen4 interface for high compute performance markets
- > HDMI for video processing applications
- > SFP28/QSFP28/RJ-45 for networking applications
- > DDR4 and LPDDR4 memory interfaces
- > FMC expansion connectors supporting a variety of optional plug-in cards

Co-Optimized Tools and Debug Method

> Fully supported by Vivado[™] Design Suite, Vitis[™] unified software platform

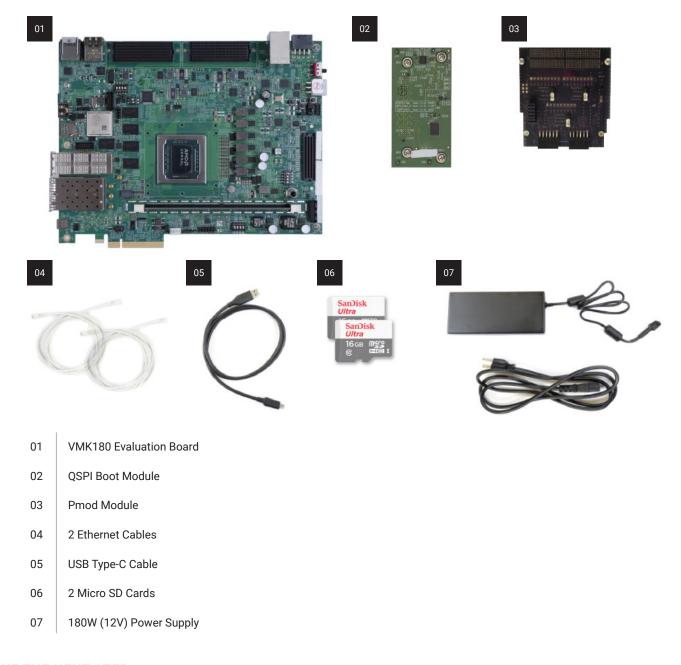


TARGET APPLICATIONS

- > Data Center Storage Acceleration
- > SmartNIC
- > 5G xHaul
- > Nx100G Ethernet & OTN Networking
- > Communication Test Equipment

KIT CONTENTS

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TAKE THE NEXT STEP

To purchase or to learn more about the Versal Prime series VMK180 evaluation kit, visit www.xilinx.com/vmk180

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